

App. No.: 09/683822
Filed: February 20, 2002
Conf. No.:7485

NOV 30 2004

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IN THE CLAIMS

1. (Currently Amended) A signal inspection device for testing any of a plurality of electronic devices that receive inputs and output outputs signals in response thereto, said signal inspection device comprising a memory circuit comprised of an EEPROM for storing an inspection program adapted to electronic devices to be inspected, said memory circuit being capable of being programmed by a personal computer external to said signal inspection device and detachably connectable thereto for programming said memory circuit to suit the electronic device to be tested, a power supply for applying input signals to an electronic device detachably connected to said signal inspection device from said signal inspection device, and a display for receiving and displaying output signals from the electronic device being tested in response to the applied input signals and created as an interactive type of displayed image on said external personal computer and transferred therefrom to said memory circuit.

2. (Previously Amended) A signal inspection device as set forth in claim 1, wherein the display is driven solely by the signal inspection device and operates independently of any personal computer.

3.-5 (Cancelled)